

New Product

P-Channel 1.5-V (G-S) MOSFET

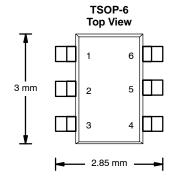
PRODUCT SUMMARY					
V _{DS} (V)	$r_{DS(on)}(\Omega)$	I _D (A)	Q _g (Typ)		
-8	0.023 @ V _{GS} = -4.5 V	-7			
	0.029 @ V _{GS} = -2.5 V	-6.2	28		
	0.036 @ V _{GS} = -1.8 V	-5.2	20		
	0.048 @ V _{GS} = -1.5 V	-5.0			

FEATURES

- TrenchFET® Power MOSFET: 1.5-V Rated
- Ultra-Low On-Resistance
- 100% R_g Tested

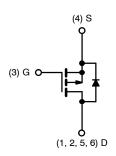
APPLICATIONS

• Load Switch for Portable Devices



Ordering Information: Si3499DV-T1—E3

Marking Code: B3xxx



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter		Symbol	5 secs	Steady State	Unit	
Drain-Source Voltage		V _{DS}	-8		V	
Gate-Source Voltage		V _{GS}	±5			
Continuous Drain Current (T _{.J} = 150°C) ^a	T _A = 25°C	ID	-7	-5.3		
Continuous Drain Current (1) = 150 C)	T _A = 85°C		-3.6	-3.9	A	
Pulsed Drain Current		I _{DM}	-20			
Continuous Diode Current (Diode Conduction) ^a		I _S	-1.7	-0.9		
Mariana Dania Dia instituta	T _A = 25°C		2.0	1.1	W	
Maximum Power Dissipation ^a	T _A = 85°C	- P _D	1.0	0.6	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 5 sec	R _{thJA}	45	62.5	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		90	110		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	25	30		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

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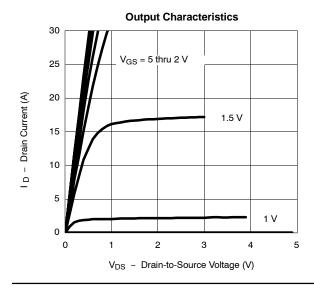


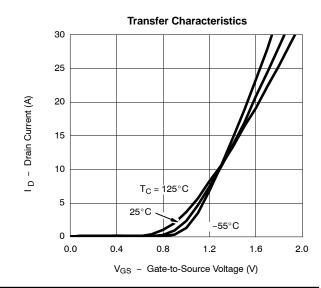
SPECIFICATIONS (T _J = 25°C UNLESS OTHERWISE NOTED)							
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.35		-0.75	٧	
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ±5 V			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = -8 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 85^{\circ}\text{C}$			-1 -10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-20			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -7 \text{ A}$		0.019	0.023		
Drain-Source On-State Resistance ^a		$V_{GS} = -2.5 \text{ V}, I_D = -6.2 \text{ A}$		0.024	0.029		
Drain-Source On-State Resistance	r _{DS(on)}	$V_{GS} = -1.8 \text{ V}, I_D = -5.2 \text{ A}$		0.028	0.036	Ω	
		$V_{GS} = -1.5 \text{ V}, I_D = -3 \text{ A}$		0.035	0.048		
Forward Transconductancea	9fs	$V_{DS} = -5 \text{ V, } I_{D} = -7 \text{ A}$		28		S	
Diode Forward Voltage ^a	V _{SD}	$I_S = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$		-0.63	-1.1	V	
Dynamic ^b	·				•		
Total Gate Charge	Qg			28	42	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = -4 \text{ V}, \ V_{GS} = -4.5 \text{ V}, \ I_D = -7 \text{ A}$		2.9			
Gate-Drain Charge	Q _{gd}			5.8			
Gate Resistance	R _g		4	8.5	13	Ω	
Turn-On Delay Time	t _{d(on)}			27	40	ns	
Rise Time	t _r	$V_{DD} = -4 \text{ V, R}_{L} = 4 \Omega$		65	100		
Turn-Off Delay Time	t _{d(off)}	V_{DD} = -4 V, R_L = 4 Ω $I_D \cong -1$ A, V_{GEN} = -4.5 V, R_g = 6 Ω		210	315		
Fall Time	t _f			110	165		
Source-Drain Reverse Recovery Time	t _{rr}	$I_F = -1.7 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}$		40	70	1	

- $\begin{array}{ll} \text{Notes} \\ \text{a.} & \text{Pulse test; pulse width} \leq 300~\mu\text{s, duty cycle} \leq 2\%. \\ \text{b.} & \text{Guaranteed by design, not subject to production testing.} \end{array}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

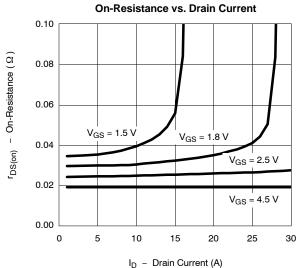




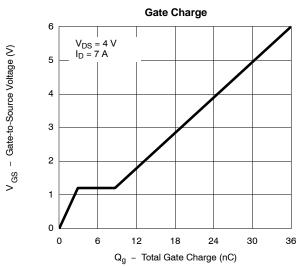


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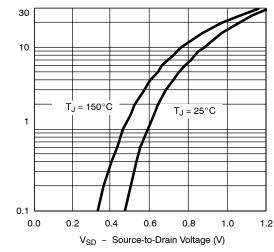
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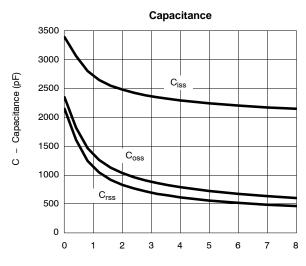




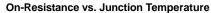


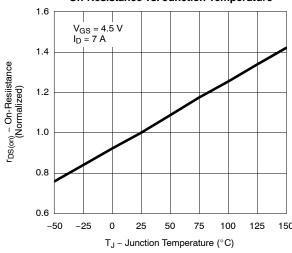
Source-Drain Diode Forward Voltage



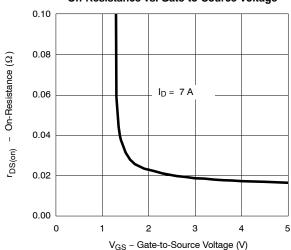


V_{DS} - Drain-to-Source Voltage (V)





On-Resistance vs. Gate-to-Source Voltage

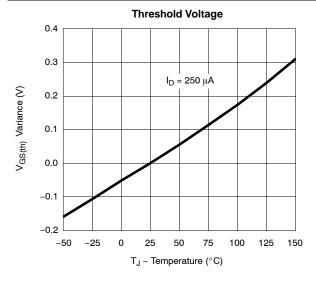


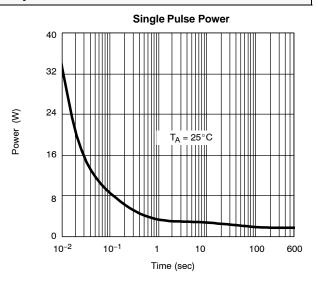
Source Current (A)

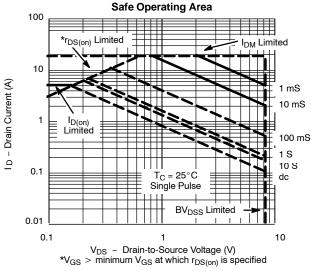
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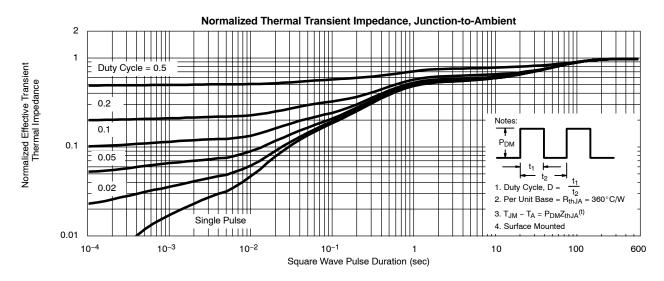


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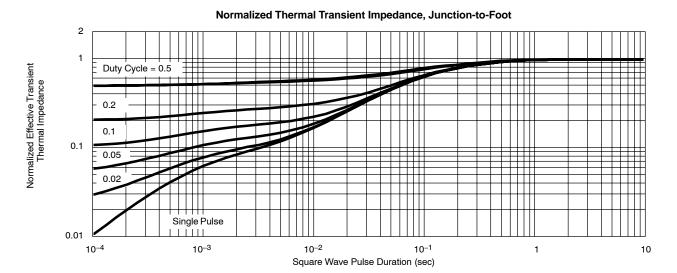








TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?73138.



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